Columbia University
Department of Electrical Engineering
EECS E4340. Final report and presentation.
Final report due: May 6, 2004
Final presentation: May 6, 2004 (last day of reading period)

1   Final report

The final report should detail your design and the design experience you had building the PDP-8. Essential elements include:

- Complete ASM charts of your controller.

- English-language-description of your controller design as well as a block diagram of the datapath. Be sure to mention any unique features that you may have incorporated into your design.

- Complete description of your front panel design.

- Discussion of your testing approach. How did you test the design in VHDL simulation? How did you test the design in hardware?

- Description of what you got to work (and what you didn't) in the lab. This will be substantiated by actually demonstrating to me things that you get working. For one or two groups, this will hopefully involve getting FOCAL to run, downloaded as a RIM image and communicating with the console TTY.

- Description of who did what. I want you to clearly indicate the contributions of each member of the group.

- Comments and criticisms for me: what problems you had, what things could have been better, what things were good.

2   Final presentation

To supplement the final presentation, I will expect each group to give a 15-minute oral presentation to the class on your design. This is not a lot of time.

Since we are all familiar with the PDP-8, you will want to use the time to highlight the more interesting parts of your design and your most compelling successes in the lab. Please prepare 5-7 slides for this (either with Powerpoint and a laptap projector, or printed on transparencies for the overhead).