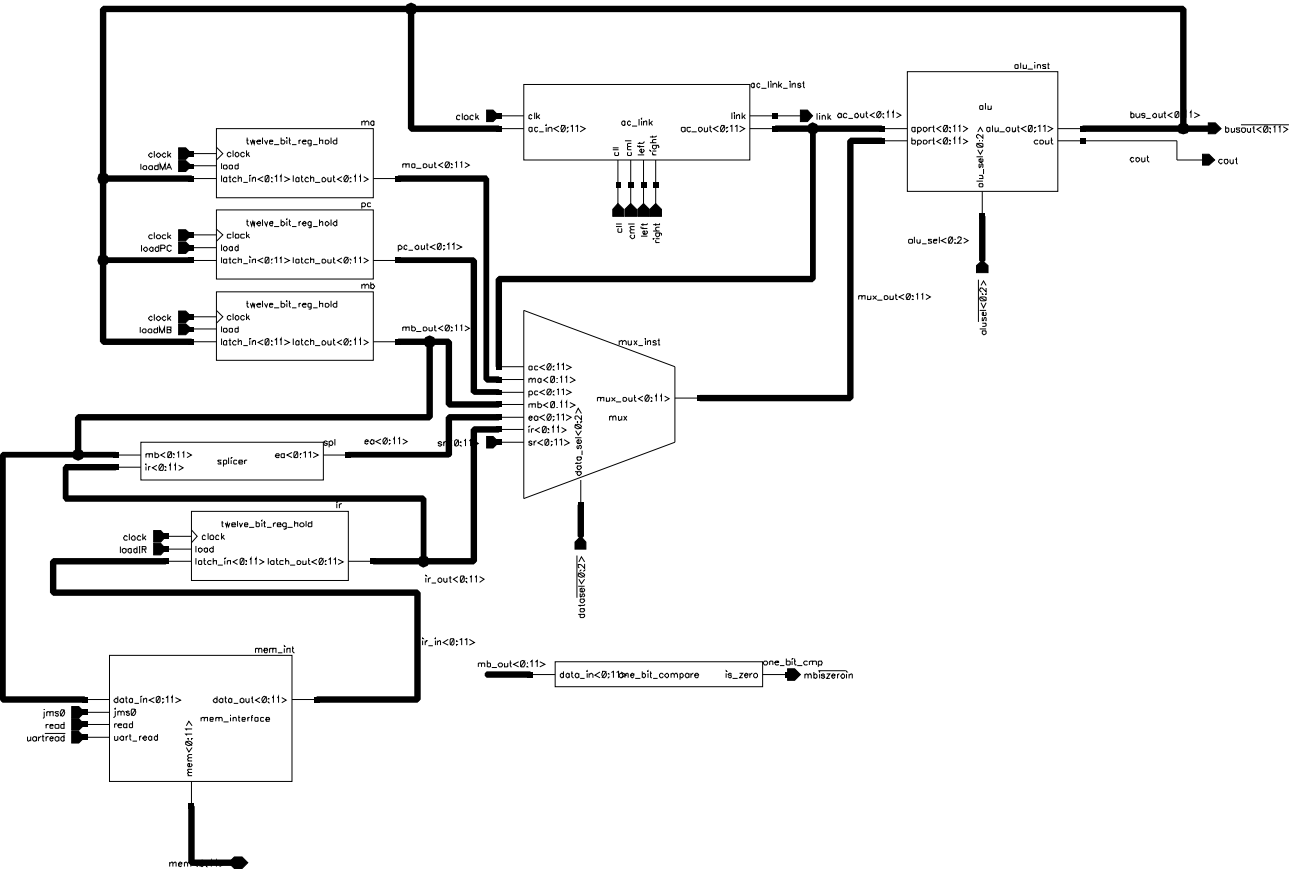
ac_link_inst
clock
clk
ac_in<0:11>
ac_link
link
ac_out<0:11>
clr
cmi
left
right
alu_inst
alu
aport<0:11>
bport<0:11>
alu_sel<0:2>
alu_out<0:11>
cout
bus_out<0:11>
busout<0:11>
ma
twelve_bit_reg_hold
clock
loadMA
load
latch_in<0:11>
latch_out<0:11>
ma_out<0:11>
pc
loadPC
pc_out<0:11>
mb
loadMB
mb_out<0:11>
mux_inst
ac<0:11>
ma<0:11>
pc<0:11>
mb<0:11>
ea<0:11>
ir<0:11>
sr<0:11>
data_sel<0:2>
mux_out<0:11>
mux
splicer
spl
ir
loadIR
ir_out<0:11>
mem_int
data_in<0:11>
data_out<0:11>
jmsr
read
uartread
mem_interface
mem<0:11>
one_bit_compare
is_zero
one_bit_cmp
mbiszeroin